

Electronic Acknowledgement Receipt

EFS ID:	1472475
Application Number:	10624398
International Application Number:	
Confirmation Number:	6797
Title of Invention:	METHOD FOR CONVERTING A PLANAR TRANSISTOR DESIGN TO A VERTICAL DOUBLE GATE TRANSISTOR DESIGN
First Named Inventor/Applicant Name:	Leo Mathew
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Filer Authorized By:	Daniel D. Hill
Attorney Docket Number:	SC12862TC
Receipt Date:	29-JAN-2007
Filing Date:	22-JUL-2003
Time Stamp:	14:55:55
Application Type:	Utility

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Request for Certificate of Correction	SC12862TC-USA-Mathew-2007-01-29-CertificateOfCorrection.pdf	150511	no	2

Warnings:

Information:	
Total Files Size (in bytes):	150511
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